

Turning Dreams into Reality



ABOUT DAVIET

DAV Institute of Engineering & Technology, Jalandhar got established in the year 2001 under the aegis of DAV College Managing Committee that is the largest non-government educational society in India managing a chain of more than 700 institutions in India and abroad. This is the first Engineering Institute of DAV College Trust and Management Society. DAVIET is an ISO 9001 -2008 certified institute.

Since its inception, DAVIET has been imparting a value based and research oriented quality technical education. We have set up a Research Foundation to promote research capabilities under various areas of Engineering & Technology and other areas of national interest. Active research is currently going on in the field of Light Wave Technology Systems, Wireless System Design, Nano-Technology, Power Quality and other contemporary areas.

DAVIET is located in the heart of city on a prime piece of land adjacent to DAV College, Jalandhar on the left side of Jalandhar-Amritsar National Highway.

Deptt. of Electronics & Communication Engg.

Department of Electronics & Communication Engineering started its education program of B.Tech with an intake of 60 students in 2001. Keeping in view the overwhelming response of admission to ECE as the first preference of the students, the department increased its intake to 120 in the year 2004.

The Punjab Technical University has established a regional centre at this institute under which ECE department is offering a Post-Graduate course leading to degree in M.Tech(ECE).

Introduction of Course Contents

This SDP shall primarily focus on MATLAB, Embedded System and VLSI Design in Robotics.

- MATLAB is a high performance language for technical computing. It integrates computation, visualization and programming in an easy to use environment, where problems and solutions are expressed in familiar mathematical notations.
- Embedded systems are specially designed for particular purposes to do the task as directed by the developer, without human intervention. The controller which embeds system properties into one single chip and it is able to take the control of the system is called Embedded System.
- VLSI Design in Robotics is a first step in the field of robotics engineering. This SDP will cover concepts related to embedded systems, artificial intelligence, robotics and automation. One will learn role of mechanical as well as electronic hardware used in making of a robot.

IMPORTANT DATES

Last date for receiving applications : 06-07-2010
 Notification about the selection : 07-07-2010
 Confirmation from participants : 10-07-2010

ORGANIZING COMMITTEE

CHIEF PATRON

Prof. C. L. Kochher, Regional Director

PATRON

Dr. A. S. Arora, Principal

CONVENER

Dr. Manoj Kumar, Professor & Head (ECE)

ORGANIZING SECRETARIES

Ms. Neeru Malhotra, Asstt. Professor (ECE)

Sh. Jagjit Malhotra, Asstt. Professor (ECE)

Sh. Jagroop Singh, Asstt. Professor (ECE)

REGISTRATION

Registration can be done online or by filling the registration form which is attached with the brochure. For filling online application form visits the website: www.davietjal.org. Last date for registration (online/ receiving the form) is July 6, 2010

Registration Form

Name

Designation

Department

Organisation/Institute

Address for Correspondence

.....

.....

Phone :

Fax No. :

E mail ID :

Accommodation Required : Yes No

(If yes provide details below)

Arrival Date and Time :

Departure Date and Time :

Are you interest to attend the tour listed below :

A tour shall be organized to visit. Golden Temple, Jallanwala Bagh and Wagah Border during this SDP. If interest, should send additional request.

Signature

Note : Registration form can also be downloaded from the website www.davietjal.org



DECLARATION

The information provided in registration form is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the programme for the entire duration. I also undertake the responsibility to inform the coordinators in case I am unable to attend the programme.

Place :

Date : Signature of the Applicant

SPONSORSHIP CERTIFICATE

This is to certify that Dr./Mr./Ms.
.....
Is an employee of our Institute/College/University and his/her application is hereby sponsored. The above information submitted by the applicant is true to my knowledge. The applicant will be permitted to attend the course, if selected and necessary leave will be granted for the purpose.

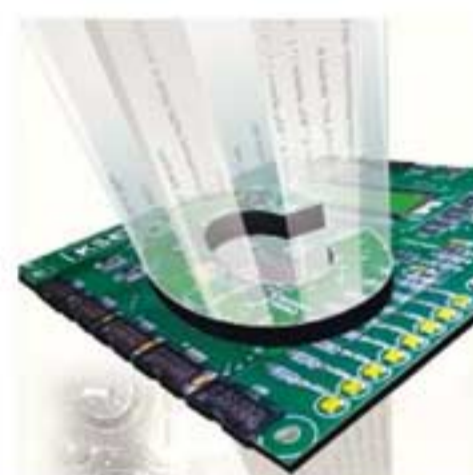
Place :

Date : Signature of the Sponsoring Authority with Seal

Note : Selected candidates will have to bring a relieving certificate from the Institute/Head of Department at time of joining the course.

DAV Institute of Engg. & Technology Kabir Nagar, Jalandhar

Embedded System & VLSI Design in Robotics 12-23 July, 2010



AGENDA

Registration : 8:45 am - 9:30 am
 Inauguration : 9:30 am - 9:45 am
 Welcome Introduction & Agenda : 9:45 am - 10:00 am

**WEEK - 1
MATLAB & Embedded System**

**WEEK - 2
VLSI Design in Robotics**

Address for Correspondence

Dr. Manoj Kumar,

Convener

Prof. & Head

Department of Electronics & Communication Engineering

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DAVIET

Cordially invites you to

AICTE Sponsored Two Weeks
Staff Development Programme
on

Embedded System & VLSI Design in Robotics

12-23 July, 2010

Organised by :

Department of Electronics & Communication Engg.

DAV Institute of Engineering & Technology

Kabir Nagar, Jalandhar

ISO : 9001:2008 Institute